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<b>TRANSMITTAL FORM</b>  (to be used for all correspondence after initial filing)	Application Number	10/676,152
	Filing Date	September 29, 2003
	First Named Inventor	Bultan et al.
	Art Unit	2611
	Examiner Name	Jayanti K. Patel
Total Number of Pages in This Submission	Attorney Docket Number	I-2-0392.1US

ENCLOSURES (Check all that apply)		
<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> After Allowance communication to Technology Center (TC)
<input type="checkbox"/> Fee Attached	<input type="checkbox"/> Licensing-related Papers	<input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences
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<input type="checkbox"/> After Final	<input type="checkbox"/> Petition to Convert to a Provisional Application	<input type="checkbox"/> Proprietary Information
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<input type="checkbox"/> Extension of Time Request	<input type="checkbox"/> Terminal Disclaimer	<input checked="" type="checkbox"/> Other Enclosure(s) (please Identify below):
<input type="checkbox"/> Express Abandonment Request	<input type="checkbox"/> Request for Refund	Form PTO-1449 (2 pgs. w/ a copy of the Supplementary European Search Report and two (2) references).
<input checked="" type="checkbox"/> Information Disclosure Statement	<input type="checkbox"/> CD, Number of CD(s) _____	
<input type="checkbox"/> Certified Copy of Priority Document(s)	Remarks	
<input type="checkbox"/> Response to Missing Parts/Incomplete Application		
<input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	Anthony L. Venezia Reg. No. 48,382 Volpe and Koenig, P.C.
Signature	<i>Anthony L. Venezia</i>
Date	September 27, 2006

CERTIFICATE OF TRANSMISSION/MAILING			
I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below.			
Typed or printed name	Anthony L. Venezia		
Signature	<i>Anthony L. Venezia</i>	Date	September 27, 2006

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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## PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the **PATENT APPLICATION** of:

Bultan et al.

**Application No.:** 10/676,152

**Confirmation No.:** 6411

**Filed:** September 29, 2003

**For:** OPTIMUM INTERPOLATOR METHOD  
AND APPARATUS FOR DIGITAL  
TIMING ADJUSTMENT

**Group:** 2611

**Examiner:** Jayanti K. Patel

**Our File:** I-2-0392.1US

**Date:** September 27, 2006

### INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Further to Applicants' Duty of Disclosure pursuant to 37 C.F.R. §1.56, Applicants wish to bring to the Examiner's attention the material cited on the enclosed PTO-1449 form. Newly cited documents are indicated by an asterisk (\*). Copies of the cited documents are enclosed.

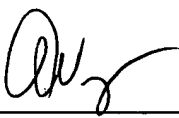
The source of the newly identified items of information is the enclosed communication from a foreign patent office in a counterpart application, dated February 2, 2006.

It is respectfully requested that the Examiner consider these documents and return an initialed copy of the PTO-1449 form indicating his consideration of the cited

materials.

Respectfully submitted,

Bultan et al.

By   
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ALV/lis  
Enclosures (4)



FORM PTO-1449  U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE  INFORMATION DISCLOSURE STATEMENT BY APPLICANT  (Use several sheets if necessary)	ATTY. DOCKET NO. I-2-0392.1US	SERIAL NO. 10/676,152
	APPLICANT Bultan et al.	
	FILING DATE September 29, 2003	GROUP 2611

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		6,313,885 B1	11/2001	Patel et al.			

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	*	1 204 215	05/2002	EP				

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		A.V. Oppenheim, "Digital Signal Processing," 1975, Prentice Hall, New Jersey.
		R.E. Crochiere et al., "Multirate Digital Signal Processing," 1983, Prentice Hall, New Jersey.
		H. Meyr et al., "Digital Communication Receivers," A Wiley-Interscience Publication, 1998, John Wiley and Sons, New York.

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS								
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)		
		F.M. Garner, "Interpolation in Digital Modems – Part I: Fundamentals," IEEE Transactions on Communications, Vol. 41, No. 3, March 1993, pgs. 501-507.
		L. Erup et al., "Interpolation in Digital Modems – Part II: Implementation and Performance," IEEE Transactions On Communications, Vol. 41, No. 6, June 1993, pgs. 998-1008.
	*	Wu, Yik-Chung et al. "FPGA Implementation of Digital Timing Recovery in Software Radio Receiver", Circuits and Systems, 2000. IEEE APCCAS 2000. The 2000 IEEE Asia-Pacific Conference on Dec. 4-6, 2000, Piscataway, NJ USA, IEEE, pp. 703-707.

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